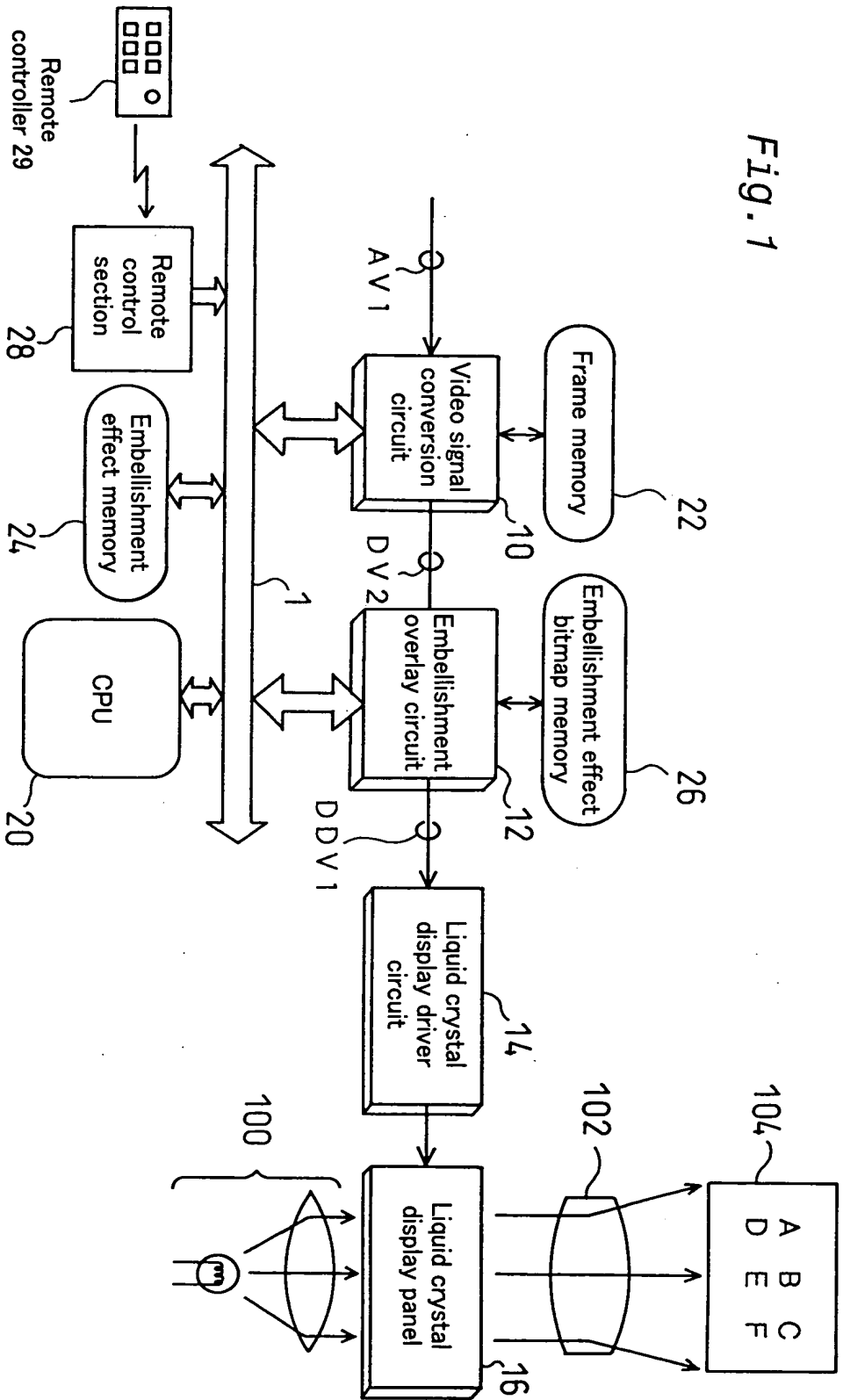


Fig. 1



Block diagram of a video processing system 10. The system includes a Frame memory 22 connected to a Video processor 34. The Video processor 34 is connected to an A-D conversion section 32. The A-D conversion section 32 is connected to a Sync separator 30. The Sync separator 30 receives RGB signal S1 and outputs Composite image signal S2 and Color signal S3. The Video processor 34 also receives DV1 and DV2 signals and outputs DCLK, WSYNC, and RSYNC signals.

Fig. 3 (a)

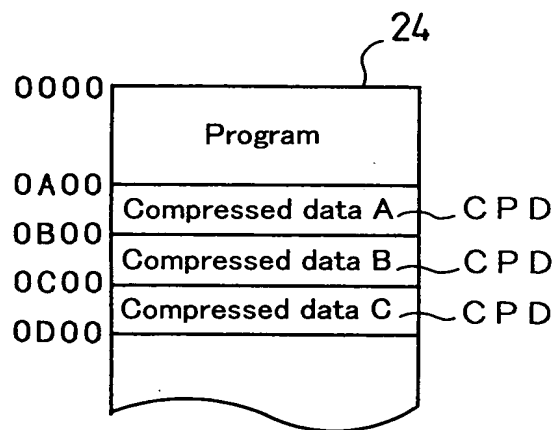


Fig. 3 (b)

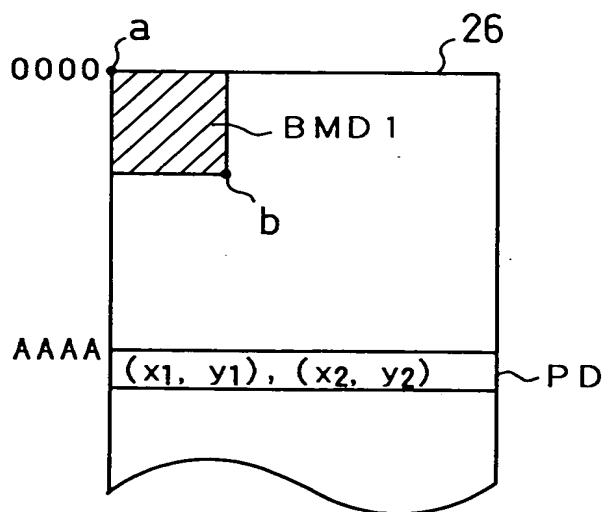


Fig. 3 (c)

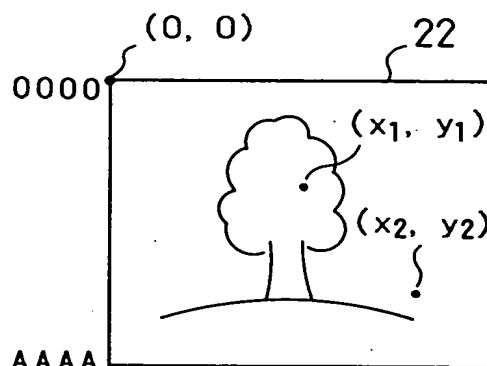


Fig. 4

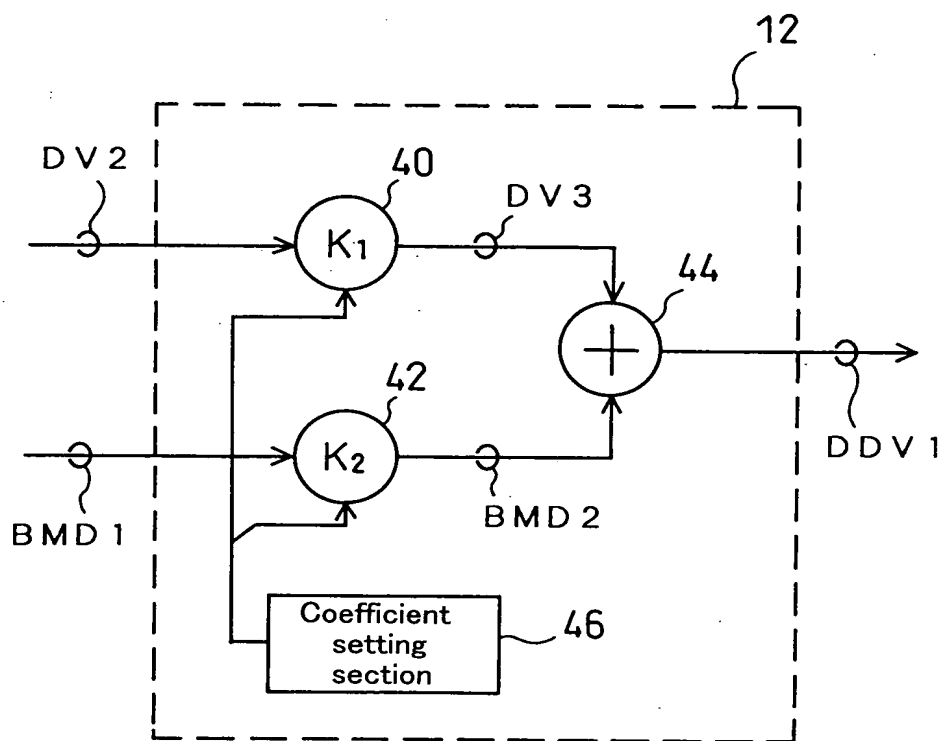


Fig.5 (a)

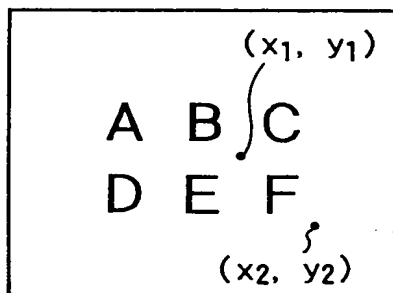


Fig.5 (c)

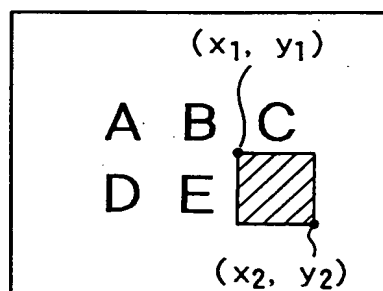


Fig.5 (b)

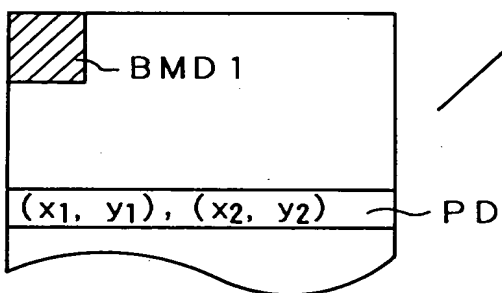


Fig.6

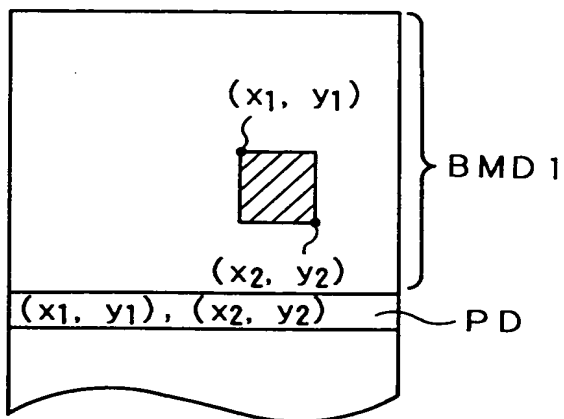


Fig. 7 (a)

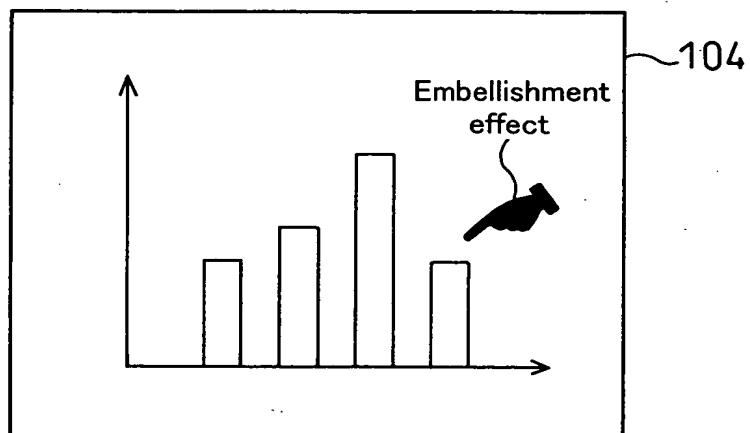


Fig. 7 (b)

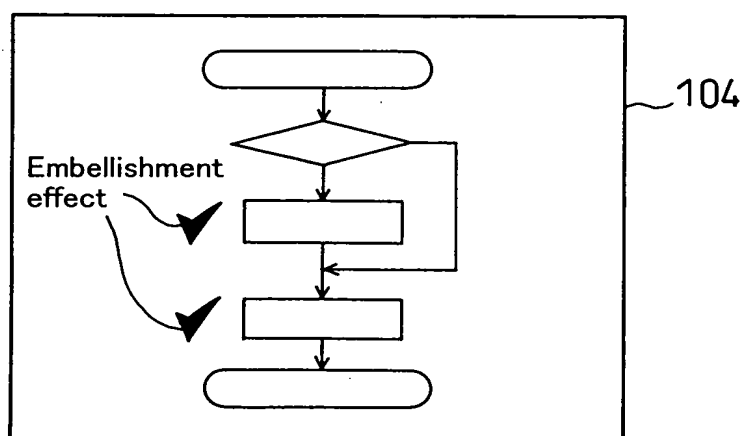


Fig. 7 (c)

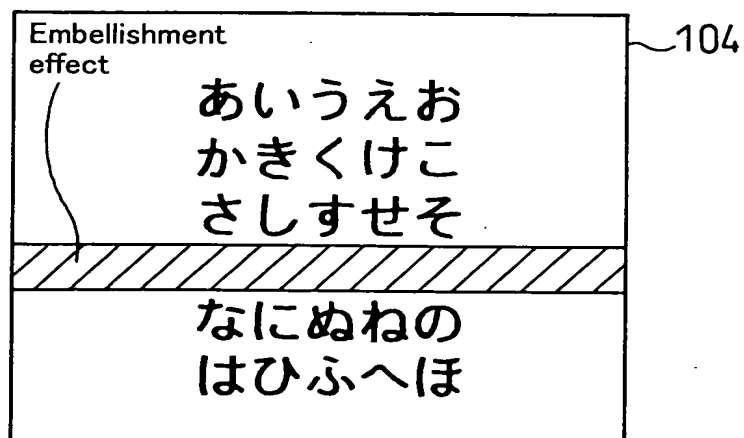


Fig. 8 (a)

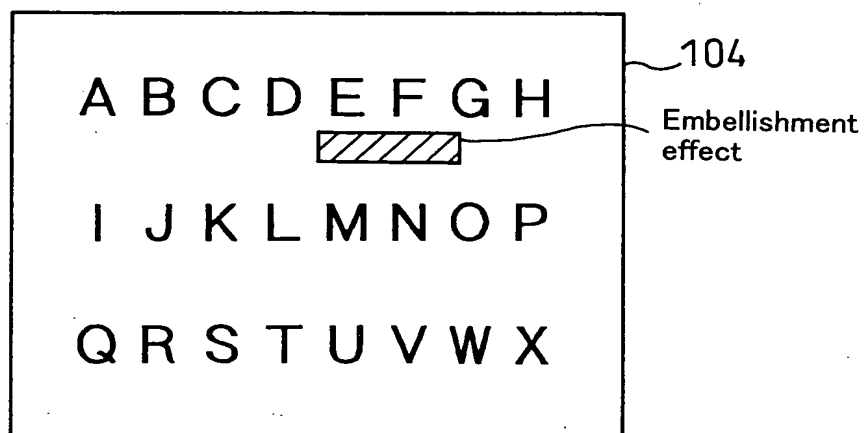


Fig. 8 (b)

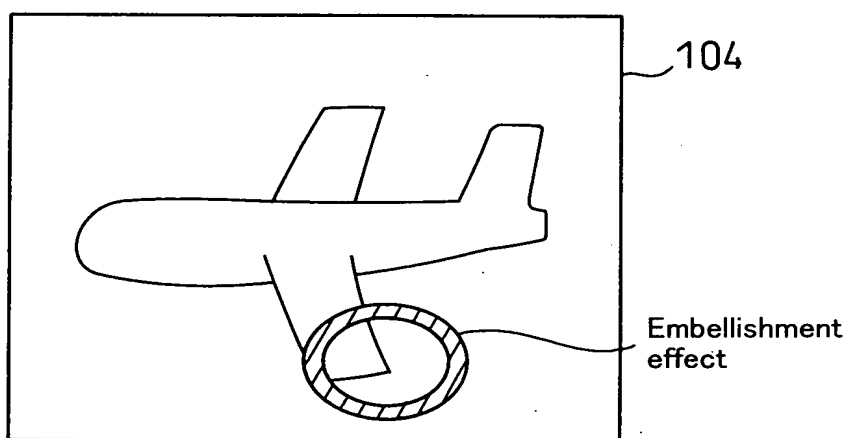


Fig. 8 (c)

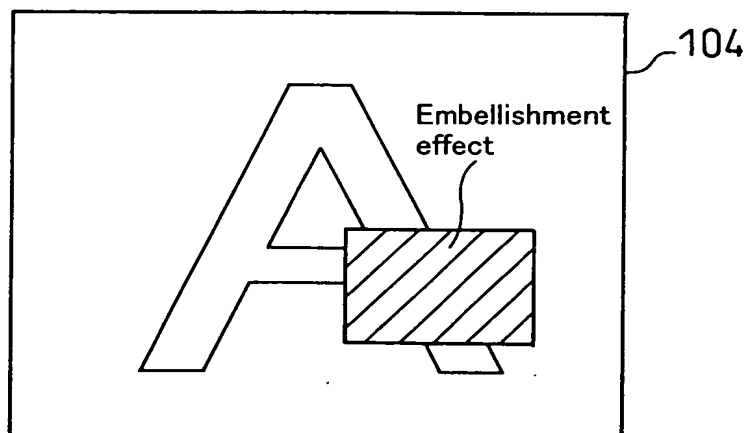


Fig. 9

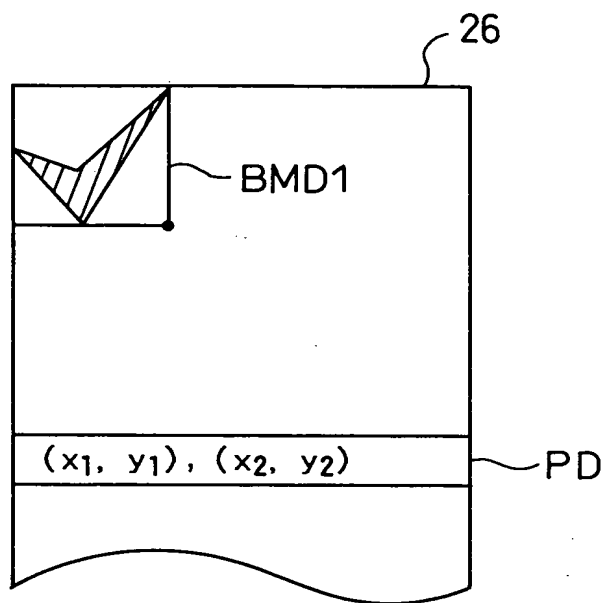


Fig. 10 (a)

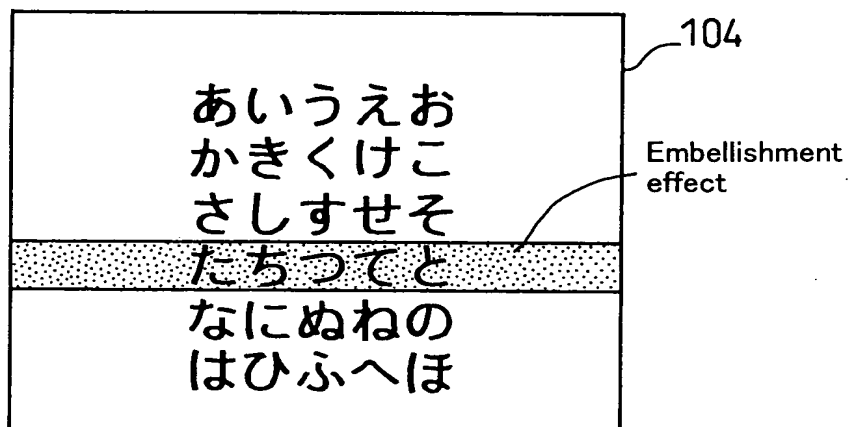


Fig. 10 (b)

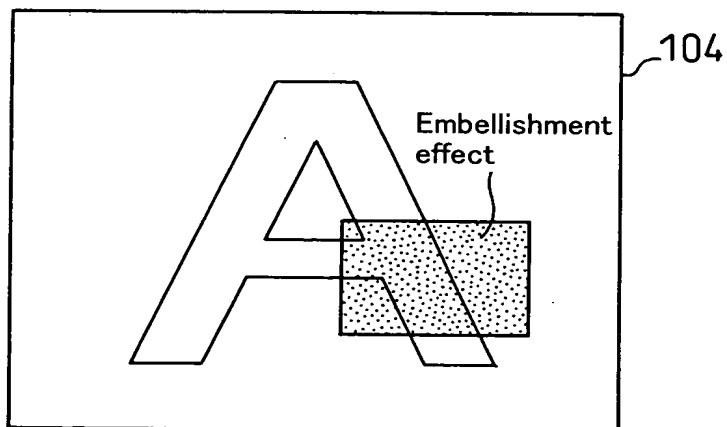


Fig. 11(a)

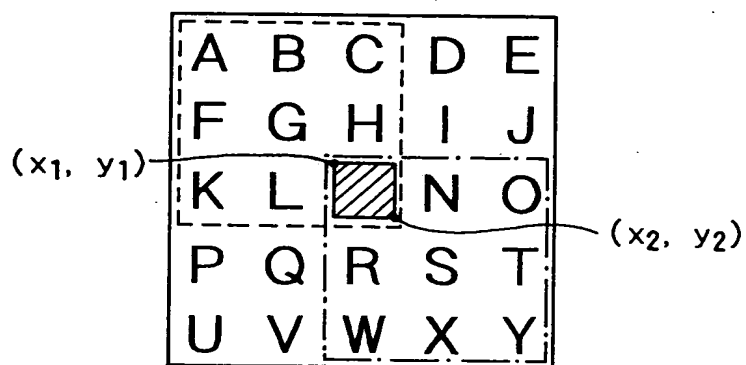


Fig. 11(b)

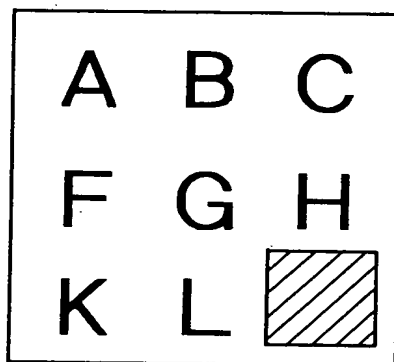


Fig. 11(c)

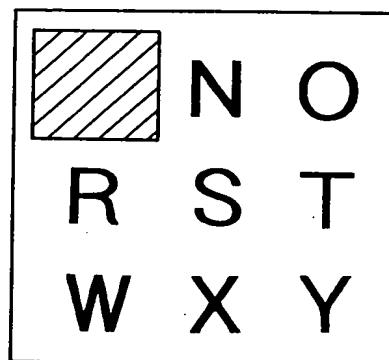
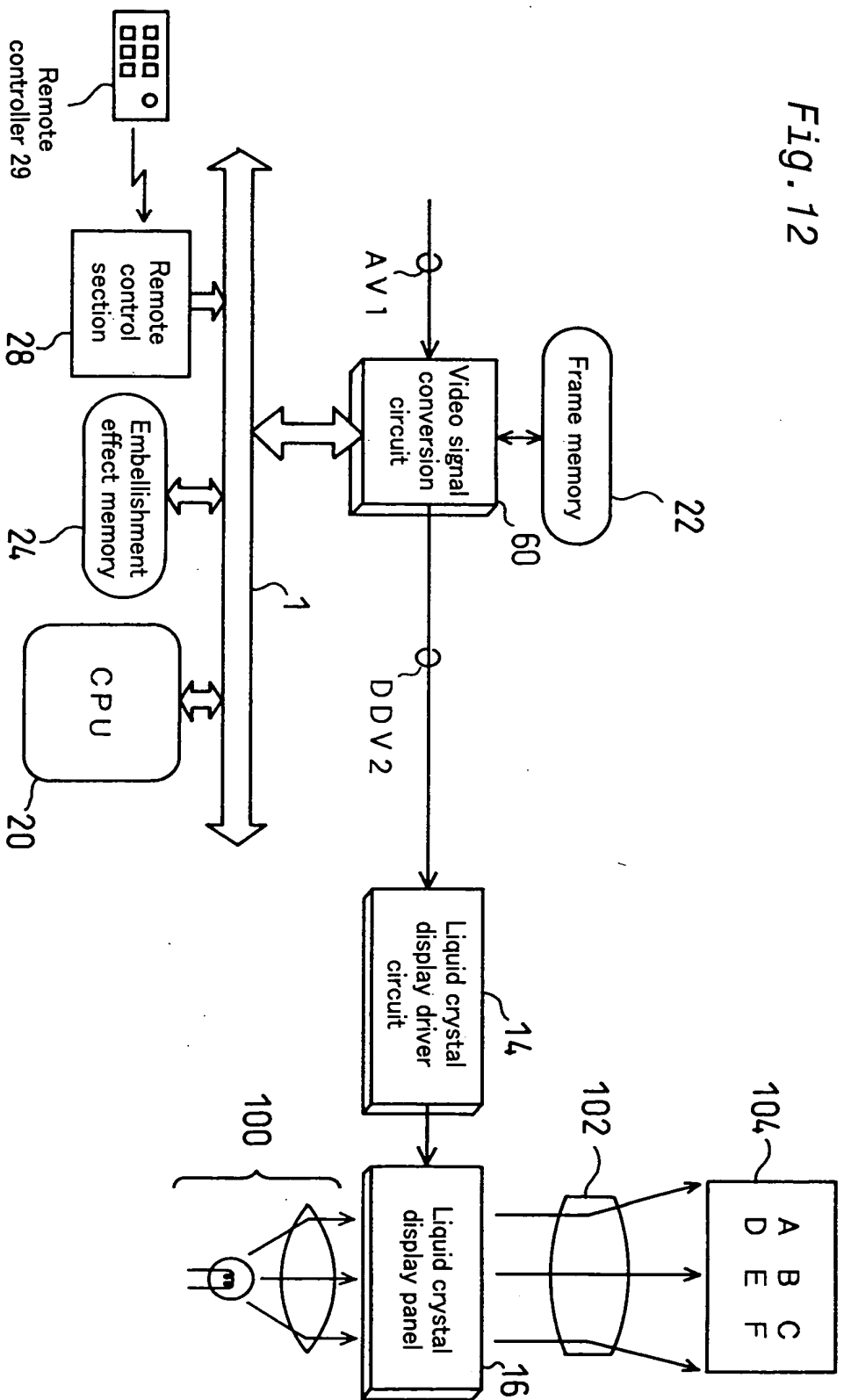


Fig. 12



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Fig. 13

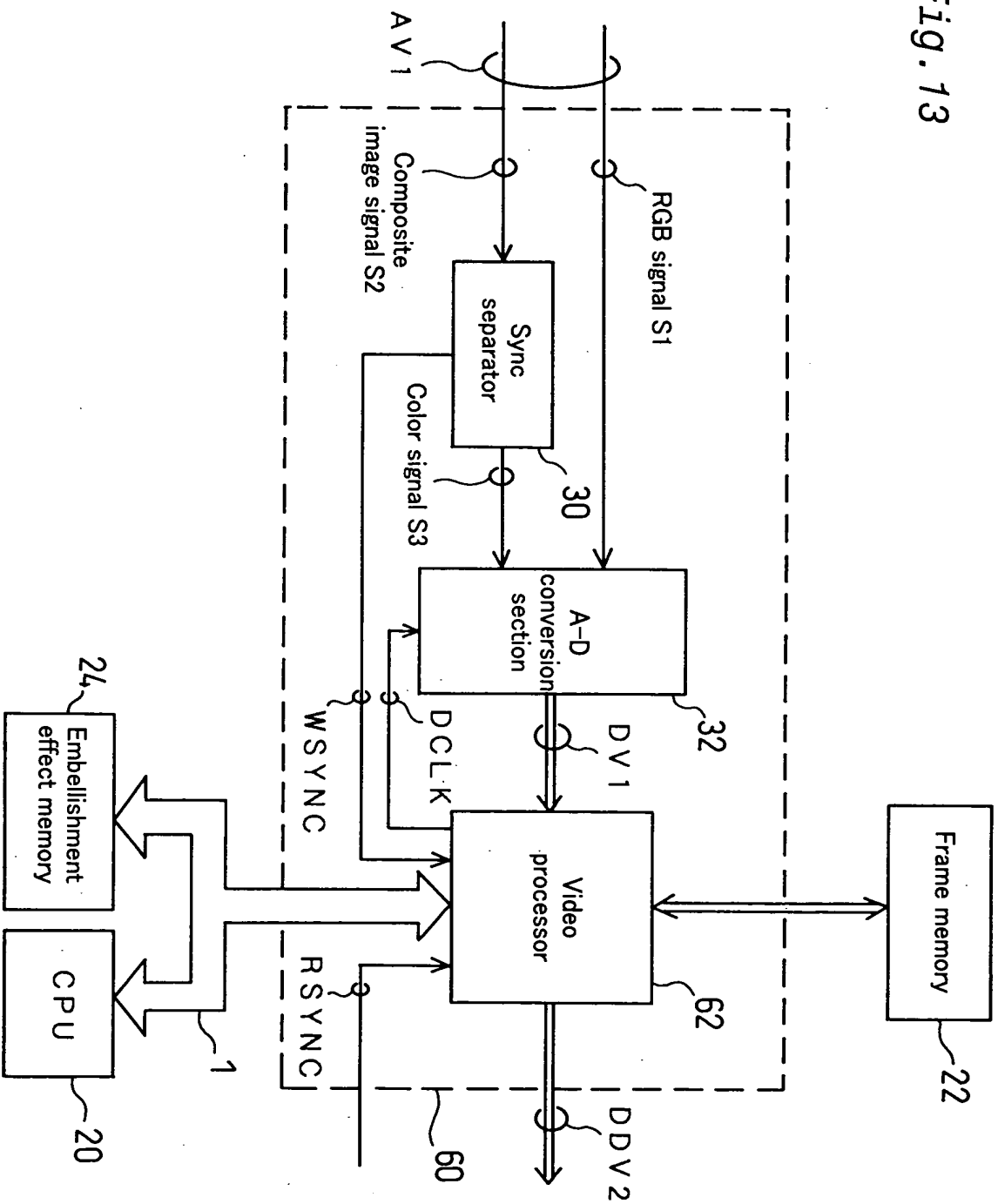


Fig. 14

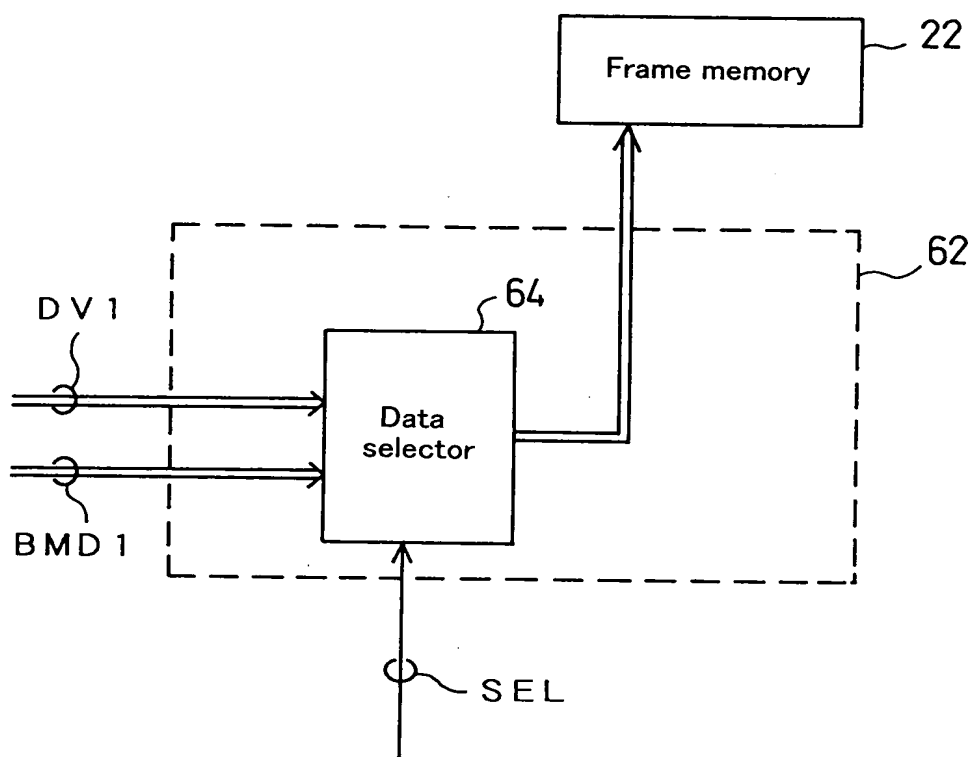




Fig. 18 (a)

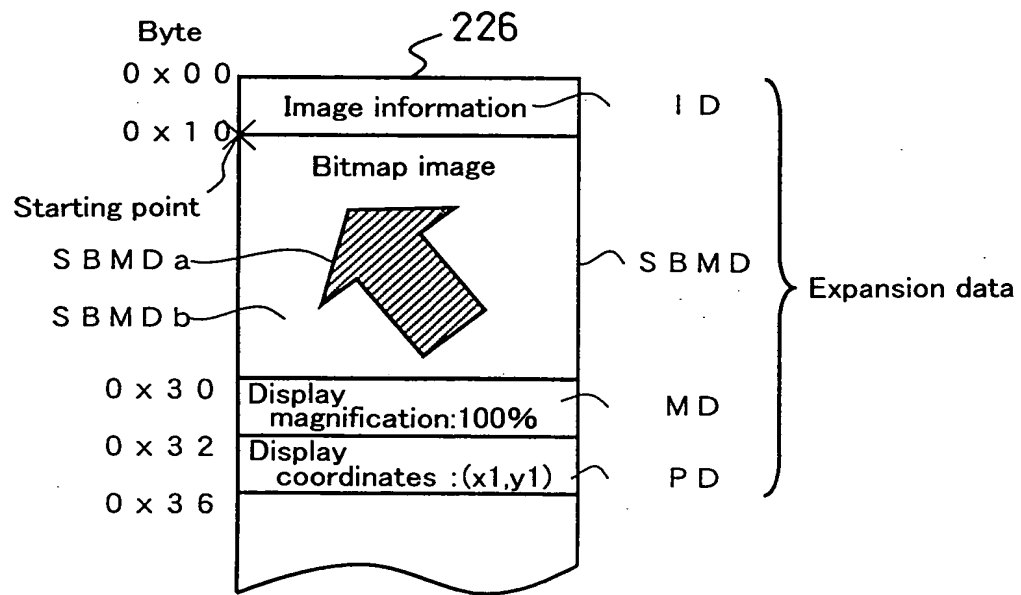
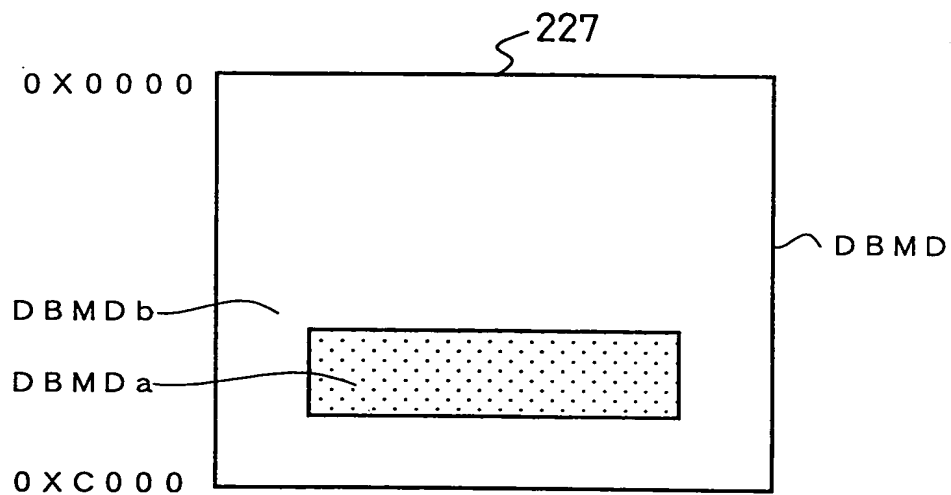


Fig. 18 (b)



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Fig. 20(a)

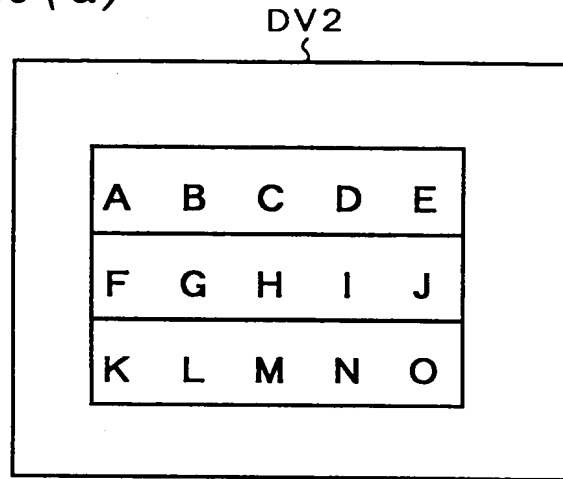


Fig. 20(b)

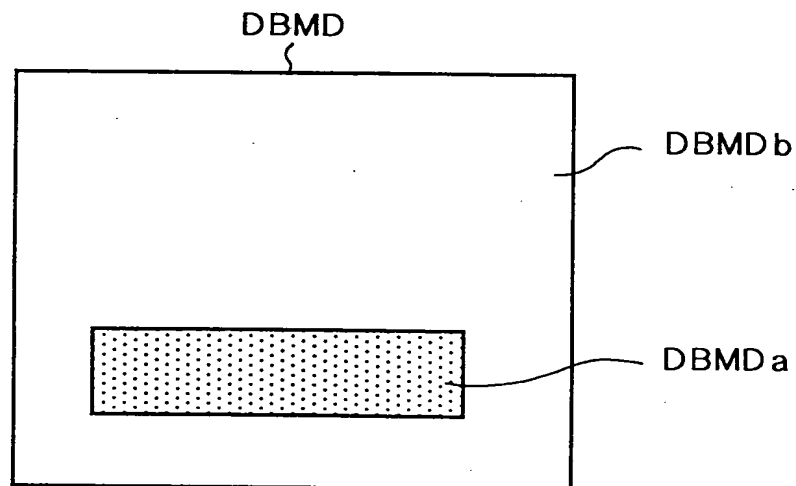


Fig. 20(c)

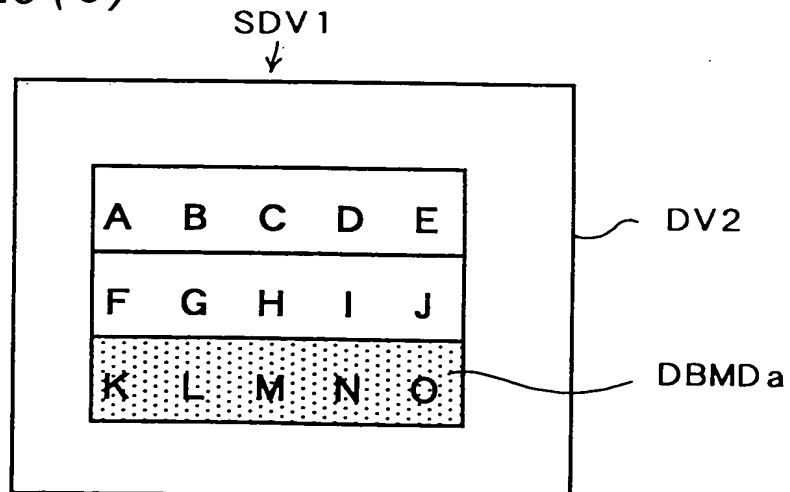


Fig.21(a)

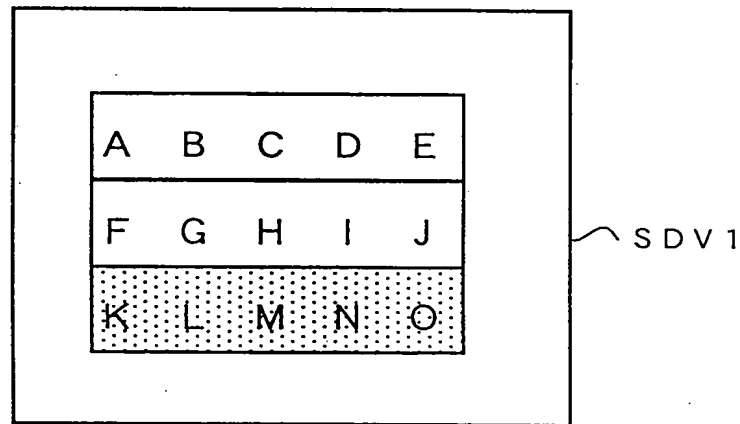


Fig.21(b)

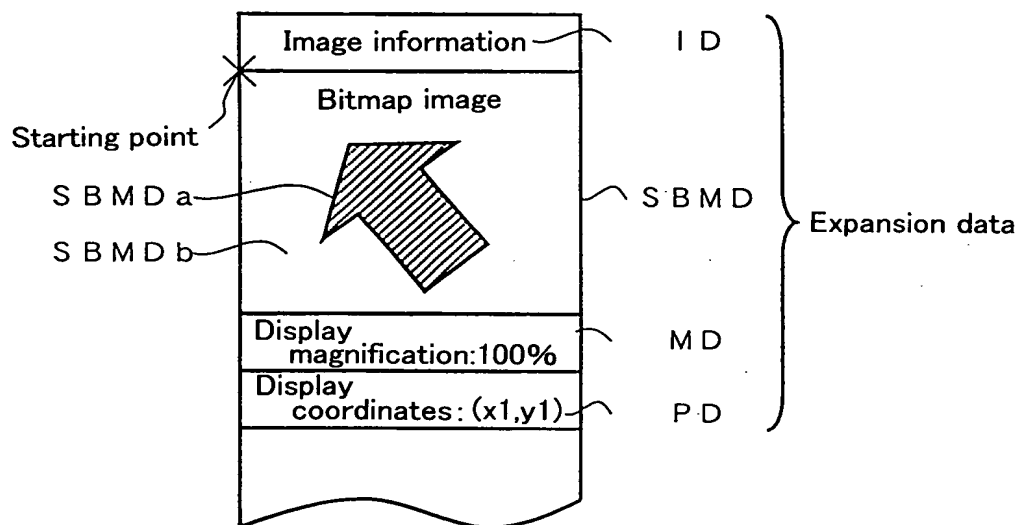


Fig.21(c)

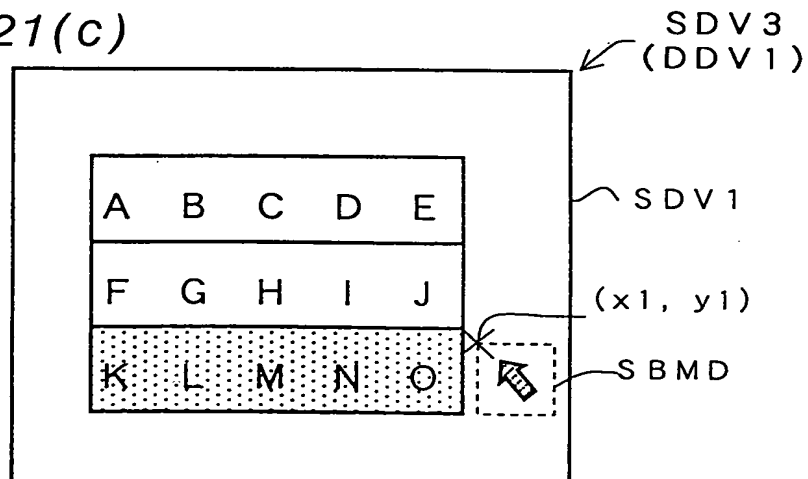


Fig. 22 (a)

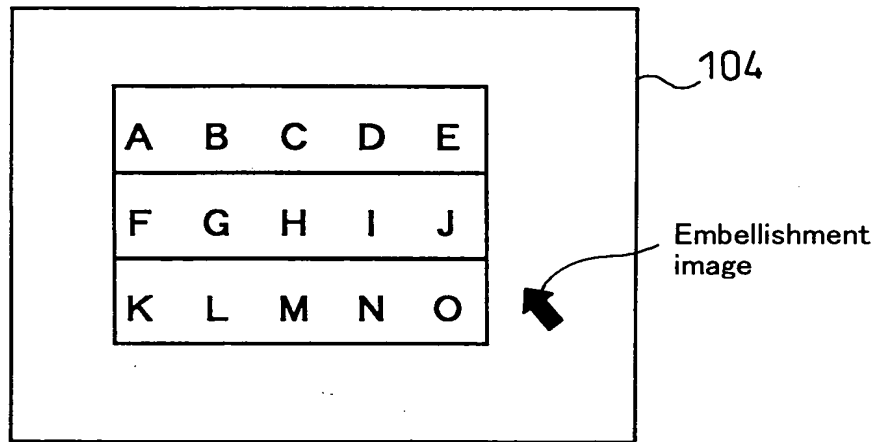


Fig. 22 (b)

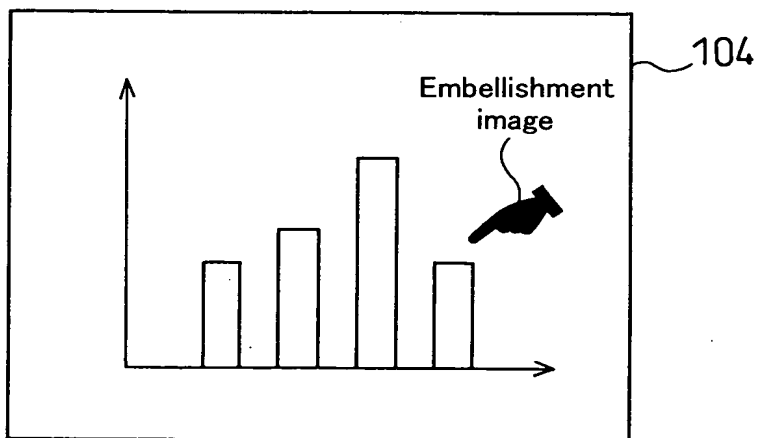


Fig. 22 (c)

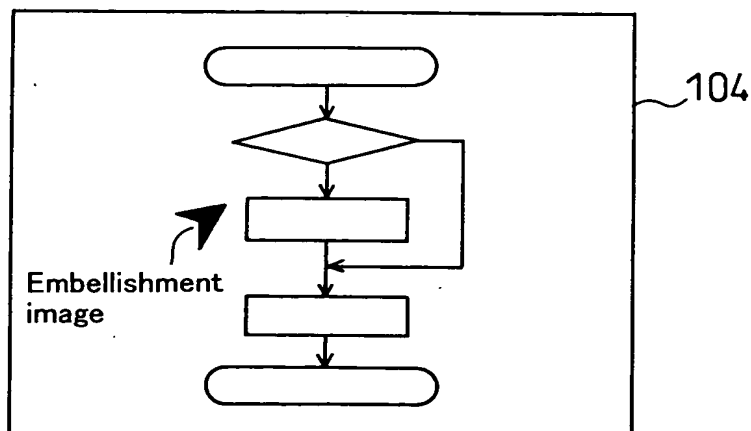


Fig. 23 (a)

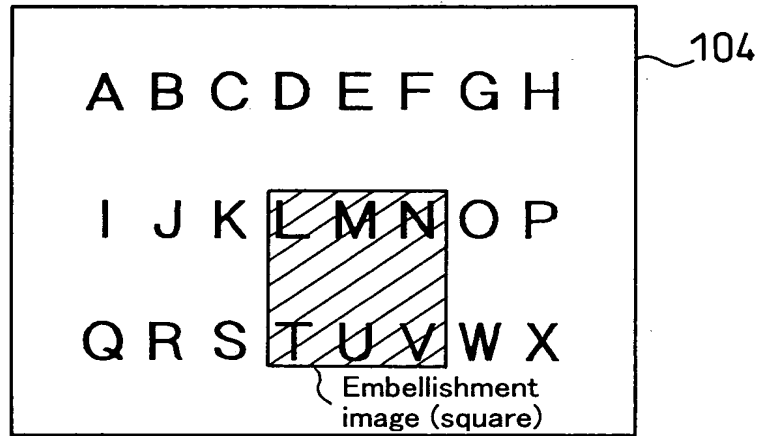


Fig. 23 (b)

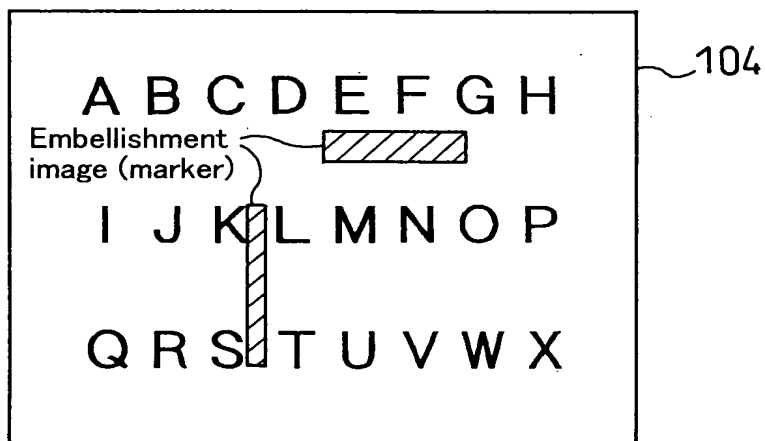


Fig. 23 (c)

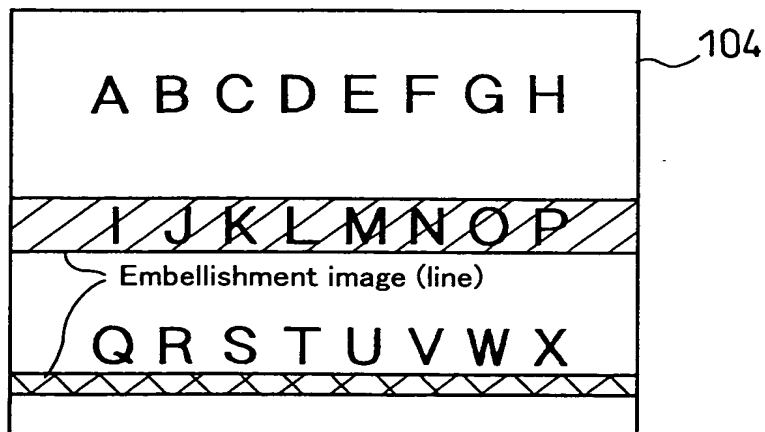


Fig. 27(a)

$$\begin{aligned} x_c &\geq x_1 \\ y_c &\geq y_1 \end{aligned}$$

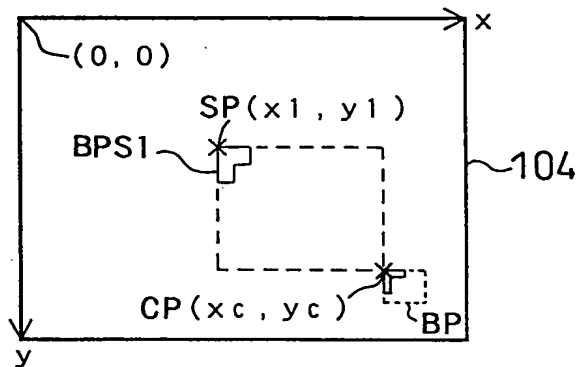


Fig. 27(b)

$$\begin{aligned} x_c &< x_1 \\ y_c &\geq y_1 \end{aligned}$$

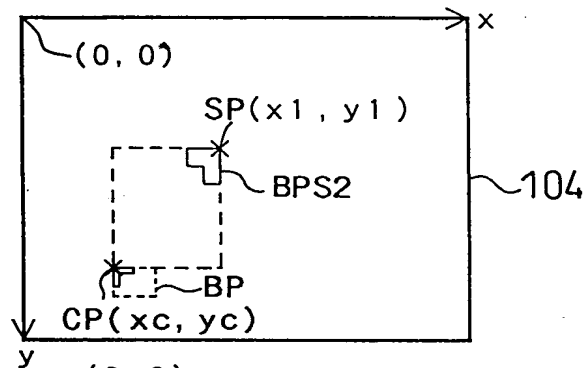


Fig. 27(c)

$$\begin{aligned} x_c &< x_1 \\ y_c &< y_1 \end{aligned}$$

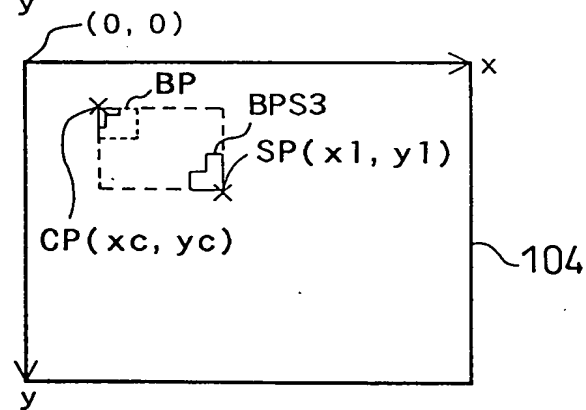


Fig. 27(d)

$$\begin{aligned} x_c &\geq x_1 \\ y_c &< y_1 \end{aligned}$$

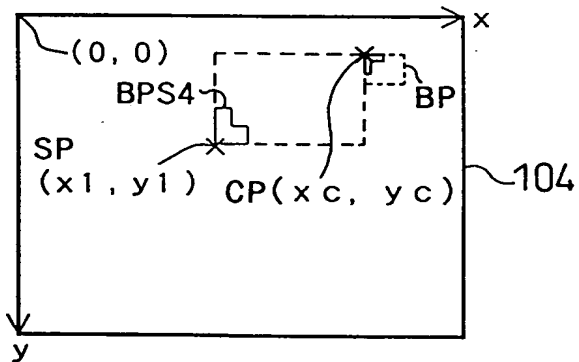


Fig. 28(a)

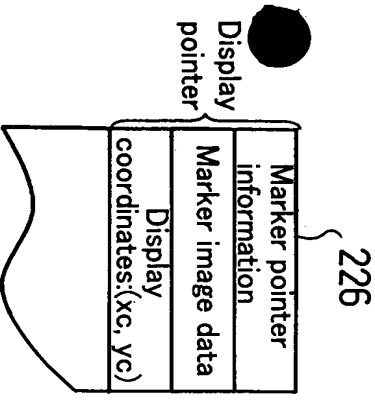


Fig. 28(b)

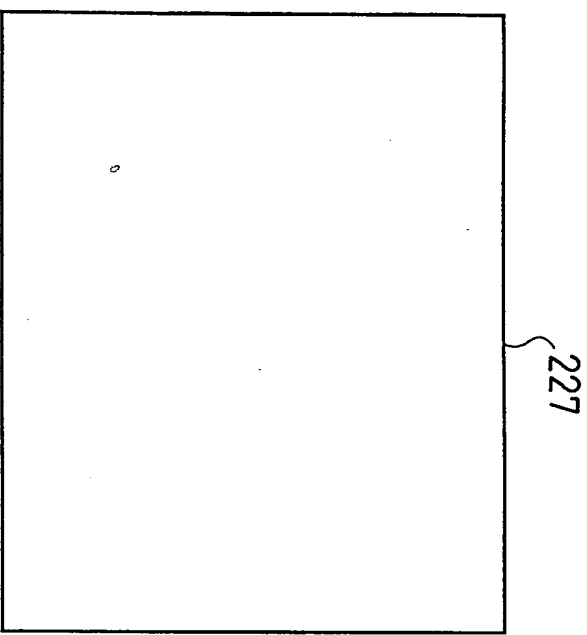


Fig. 28(c)

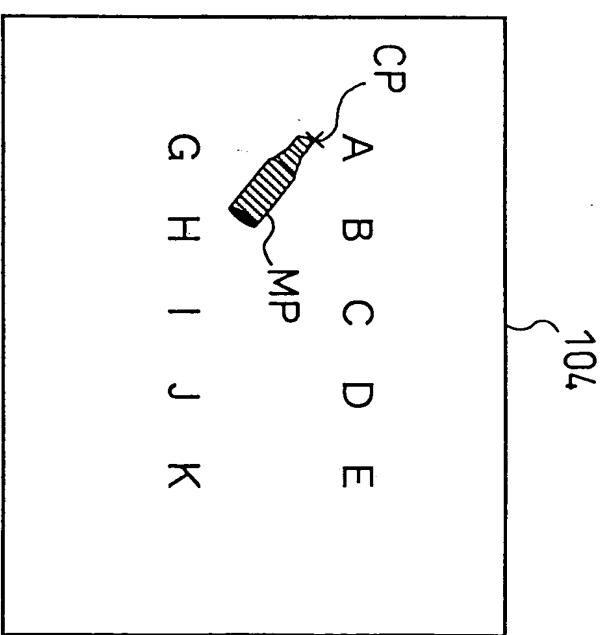


Fig. 31(a)

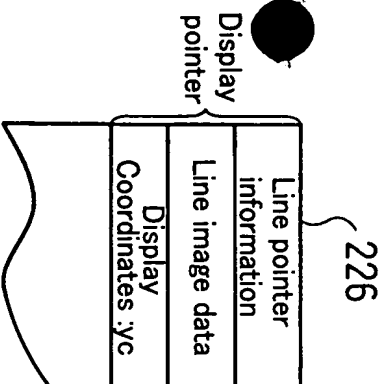


Fig. 31(b)

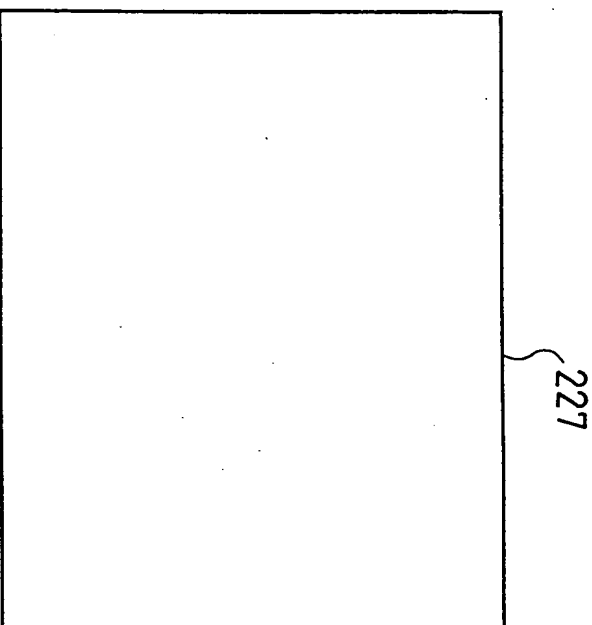


Fig. 31(c)

